

**IN THE CLAIMS**

1. (Original) A high frequency power amplifier having a plurality of amplifying systems, each of said amplifying systems comprising:

an input terminal to which a signal to be amplified is supplied;

an output terminal;

a bias terminal;

a plurality of amplifying stages which are sequentially cascaded between said input terminal and output terminal; and

a bias circuit connected to said bias terminal and each of said amplifying stages to apply a bias potential to said amplifying stage, said amplifying stage including a control terminal for receiving an input signal and said bias potential supplied to the stage and a first terminal for transmitting an output signal of the stage, a first amplifying systems and a second amplifying stage of each of said amplifying stages being monolithically formed on a single semiconductor chip, a part of bias resistors included in the bias circuits of said first amplifying stage and second amplifying stage being monolithically formed on said semiconductor chip.

Claims 2-14 (Canceled)